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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 09/577,671 05/25/2000 PAT 6,737,341

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 11-158089 06/04/1999

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 04/27/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after allowance	JAPAN	DRAWING 27	13	3
Verified and Acknowledged	<i>M. Yamamoto</i> Examiner's Signature	Initials			

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## TITLE

Semiconductor integrated circuit device and method for manufacturing the same

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